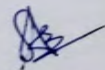
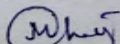


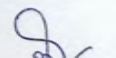
HERITAGE INSTITUTE OF TECHNOLOGY
TIME TABLE
APPLIED ELECTRONICS AND INSTRUMENTATION ENGINEERING
B. Tech 2nd Year 1st Semester Lateral Entry **SESSION: 2025-2026**

DAY	9.00 AM – 10.00 AM	10.00 AM – 11.00 AM	11.00 AM – 12 NOON	12 NOON – 1.00 PM	1.00 PM – 2.00 PM	2.00 PM – 3.00 PM	3.00 PM – 4.00 PM	4.00 PM – 5.00 PM
MON	P/AEI2152/SJB+SKD/ SKB+HP /ICT001+ICT102			BREAK	L/PHY2101/AC/ICT108	L/AEI2103 RSG/ICT108	P/AEI2154/AKB+AMS/BM+SKB /ICT005	
TUE	L/AEI2102/SJB/ICT108	L/AEI2103 RSG/ICT108	L/AEI2101 PPS/ICT108	L/LAEI2104 AKB/ICT108	BREAK	P/AEI2150/MC+DC/BM+SMP /ICT102		
WED	L/AEI2101 PPS/ICT108	L/AEI2103 RSG/ICT108	L/AEI2102/SJB/ICT108	L/PHY2101 NPN/ICT108	BREAK	T/AEI2103 RSG/ICT108	Mentoring	
THU	P/AEI2153/RSG+SMC/SKB+SJD /ICT009			L/AEI2104 AKB/ICT108	BREAK	P/AEI2151/PPS+SJB/SBJ+BM/ICT001		
FRI	L/AEI2104 AKB/ICT108	L/PHY2101 NPN/ICT108	L/AEI2102 SJB/ICT108	BREAK	L/AEI2101 PPS/ICT108	L/PHY2101 AC/ICT108	Remedial Class ICT108	

To be Effective from the date of commencement of lateral entry classes
A minimum of 75% attendance is mandatory for being eligible to sit for the End-Semester Examination
Students should target 100% attendance


Member


HOD


Principal

HERITAGE INSTITUTE OF TECHNOLOGY

TIME TABLE

APPLIED ELECTRONICS AND INSTRUMENTATION ENGINEERING

SESSION: 2025-2026

B. Tech 2nd Year 1st Semester Lateral Entry

TA Details:

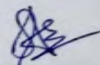
Faculty Details:

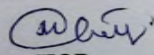
INITIALS	FULLNAME	DEPT	INITIALS	FULLNAME	DEPT
MC	Prof. (Dr.) Madhurima Chattopadhyay	AEIE	SJD	Mr. Sujoy Kr. Das	AEIE
SJB	Prof. (Dr.) Surajit Bagchi		HP	Ms. Harapriya Panda	
PPS	Prof. (Dr.) Pradip Saha		SKB	Mr. Sumit Kr. Bakshi	
RSG	Prof. Reshma Sengupta		SMP	Ms. Sampa Paul	
AKB	Prof. (Dr.) Anil Kumar Bag		BM	Mr. Bibhas Maikap	
DC	Prof. (Dr.) Debjyoti Chowdhury		SBJ	Ms. Suparna Banerjee	
SKD	Prof. (Dr.) Soumik Das				
SMC	Prof. (Dr.) Samik Chakraborty				
AMS	Prof. Arindam Sarkar				
NPN	Prof. (Dr.) N. P. Nayak	Physics			
AC	Prof. (Dr.) Arindam Chakraborty				

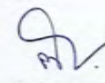
To be Effective from the date of commencement of lateral entry classes

A minimum of 75% attendance is mandatory for being eligible to sit for the End-Semester Examination

Students should target 100% attendance


Member


HOD


Principal

HERITAGE INSTITUTE OF TECHNOLOGY
TIME TABLE
APPLIED ELECTRONICS AND INSTRUMENTATION ENGINEERING
B. Tech 2nd Year 1st Semester Lateral Entry **SESSION: 2025-2026**

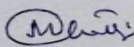
COURSE STRUCTURE:

2nd Year 1st Semester

A. Theory							
Sl.	Code	Subject	Contacts Periods/ Week				Credit Points
			L	T	P	Total	
1	PHY2101	Physics II	3	0	0	3	3
2	AEI2101	Analog Electronics	3	0	0	3	3
3	AEI2102	Circuit Theory & Network Analysis	3	0	0	3	3
4	AEI2103	Electrical & Electronic Measurements	3	1	0	4	4
5	AEI2104	Sensors and Transducers	3	0	0	3	3
Total Theory			15	1	0	16	16
B. Practical							
1	AEI2150	Design Thinking & Idea Lab Workshop	0	0	2	2	1
2	AEI2151	Analog Electronics Lab	0	0	3	3	1.5
3	AEI2152	Circuits & Networks Lab	0	0	3	3	1.5
4	AEI2153	Electrical & Electronic Measurements Lab	0	0	3	3	1.5
5	AEI2154	Sensors and Transducers Lab	0	0	2	2	1
Total Practical			0	0	13	13	6.5
Total of Semester			15	1	13	29	22.5

To be Effective from the date of commencement of lateral entry classes
A minimum of 75% attendance is mandatory for being eligible to sit for the End-Semester Examination
Students should target 100% attendance


Member


HOD


Principal